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**This is a pre print version of the following article:**

*Original Citation:*

*Availability:*

This version is available <http://hdl.handle.net/2318/1930970> since 2023-09-08T10:05:16Z

*Published version:*

DOI:10.1016/j.nima.2023.168521

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# Development of a SiPM Pixel Prototype for the Large-Sized Telescope of the Cherenkov Telescope Array

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## Abstract

The Cherenkov Telescope Array (CTA) will be the next generation ground-based gamma-ray observatory. CTA consists of different telescope types of which the largest ones (Large-Sized Telescopes, LSTs) cover the lower energy range, between 20 GeV and 200 GeV. The first LST is currently being commissioned at the Roque de los Muchachos Observatory, La Palma, Canary Islands. Its camera has 1855 photomultipliers (PMTs) with 1.5 inch cathodes. Silicon Photomultipliers (SiPMs) are increasingly becoming valid alternatives to PMTs also in gamma-ray astronomy. In the context of the LST project, there is an effort to study a novel Advanced Camera, equipped with SiPMs and a completely redesigned electronics based on a fully digital approach. To study and develop solutions on the sensors of these camera, we built a prototype camera module with a fully re-designed pre-amplifying stage and sensor bias control while re-using the digitizing and triggering stages of the existing LST camera module. We report on the design choices made to achieve the highest performance in terms of timing and charge resolution and the laboratory measurements validating those choices.

**Keywords:** Silicon Photomultipliers, Gamma-Ray Astronomy, Imaging Atmospheric Cherenkov Telescope, Front-end electronics, Electronics, Low light detectors

## 1. Introduction

Very-high-energy (VHE) gamma rays are excellent probes to study and understand the origin of cosmic rays and the mechanisms of particle acceleration in extreme environments. Imaging Atmospheric Cherenkov Telescopes (IACTs) have evolved into the most sensitive instruments in VHE gamma-ray astrophysics. Multiple telescopes acquire stereoscopic images of extensive air showers (EAS) induced by gamma rays in the atmosphere by capturing Cherenkov light.

The Cherenkov Telescope Array (CTA) will be the most sensitive ground-based gamma-rays observatory in the energy range between 20 GeV and 300 TeV. It will be built at two sites, one in each hemisphere, to achieve full-sky coverage. At each site, IACTs of different sizes ensure coverage of the entire energy range. The largest IACTs (Large-Sized Telescopes, LSTs) will focus on the lower energy range (20 GeV - 200 GeV). Currently, the first LST is being commissioned at the northern site, the Roque de Los Muchachos Observatory, La Palma, Canary Islands (Spain) [1].

The LST's 23 m diameter segmented light-collection surface focuses the Cherenkov light on a camera with 1855 photomultipliers (PMTs) with 1.5 inch diameter photocathodes. Due to

their high photon detection efficiency and tolerance to high illumination levels, Silicon Photomultipliers (SiPMs) are viable alternatives to PMTs in IACTs. In CTA, both small-sized and dual-mirror medium-sized telescopes will use SiPMs [2, 3].

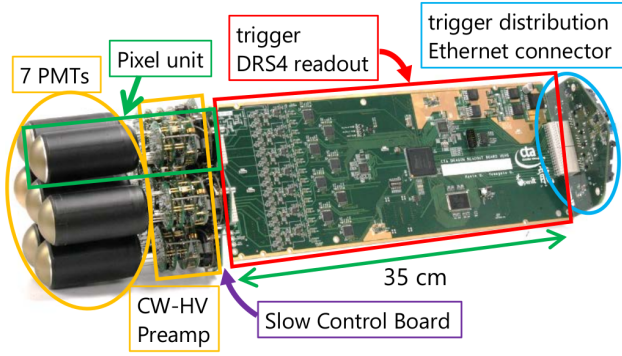
We are working towards replacing PMTs with SiPMs also in LSTs. The idea is not only to replace the PMTs with SiPMs, but to completely redesign the electronics with a fully digital approach [4].

As a proof-of-concept, we built a SiPM-based module prototype, starting with the current camera configuration and electronics. The high night sky background (NSB) rate (expected to be around 1.5 GHz per pixel in dark condition for our prototype) puts constraints on how long the electrical signals must be. Indeed, the longer the signals, the higher the trigger threshold must be in order to not be dominated by accidental triggers due to the pile up of overlapping NSB signals. To reach thresholds that are competitive with PMTs, it was our goal to design the front-end electronics such that the processed SiPM signals are as short as the PMT signals in the current camera.

A similar effort is ongoing in the Major Atmospheric Gamma Imaging Cherenkov (MAGIC) Collaboration, where SiPM modules are installed for direct comparison with PMTs in one of the two telescopes [5, 6].

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**Figure 1:** LST camera module [8]. Only the Pixel Unit (consisting of the Cockcroft-Walton generator, the Preamplifier and the PMT) is replaced with a SiPM pixel.

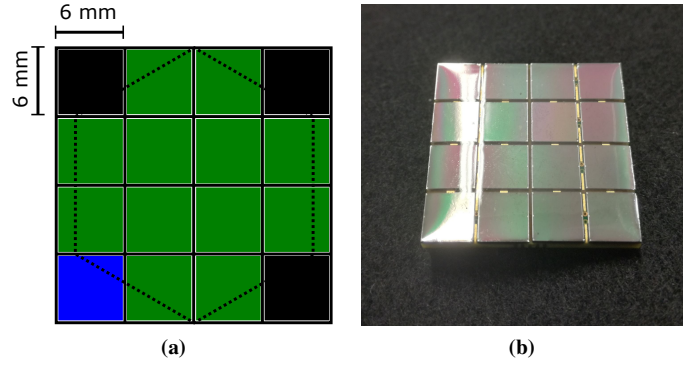
## 2. SiPM-based prototype for the LST camera

The aim of this work is to build a pixel prototype for a SiPM-based camera for the LST telescope of the CTA observatory. Since our plan was to study only the pixel, the preamplifier and the slow control, we kept most of the existing camera readout. Referring to Figure 1, only the photo-sensors, their power supply, and the pre-amplifying stage are modified while the rest of the camera would remain unchanged [7].

With our work we wanted to demonstrate some of the main advantages of SiPMs over PMTs in our application, i.e. their robustness and tolerance to high illumination levels. Although SiPMs can withstand currents of several milliamps, the front-end electronics usually cannot. Therefore, it is necessary to limit the current flowing into the SiPM. The usual method is to use a protective resistor on the power line. However, this approach has a significant drawback of making the SiPM bias voltage dependent on the current flowing through it, thereby affecting its gain, dark count rate, optical crosstalk, and photon detection efficiency. To overcome this problem, we developed an alternative solution using a transistor-based current limiter.

In order to reach sensitive areas comparable to one PMT, the signals of at least twelve  $6\text{ mm} \times 6\text{ mm}$  SiPMs need to be summed. To speed-up the development, we used an already existing  $4 \times 4$  SiPM array, connecting only twelve of the sixteen SiPMs. Figure 2a shows which twelve pixels are summed in the frontend electronics to form one effective camera pixel. One of the corner pixels is used for calibration purposes. The dashed line shows the inner area of the LST Winston cone, used to concentrate the light onto the photomultipliers and reduce the dead space on the focal plane [9]. The sensors used in this prototype are the NUV HD3-4 from the Fondazione Bruno Kessler (FBK), developed for the mid-size Schwarzschild-Couder Telescope (SCT) of the Cherenkov Telescope Array (CTA) [10]. A picture of the SiPM array is shown in Figure 2b. For the final Advanced Camera, we will optimize the matrix layout and the SiPM characteristics.

Given the large number of channels, we picked the Multiple Use SiPM Integrated Circuit (MUSIC), an Application Specific Integrated Circuit (ASIC), to realize the front-end electronics.



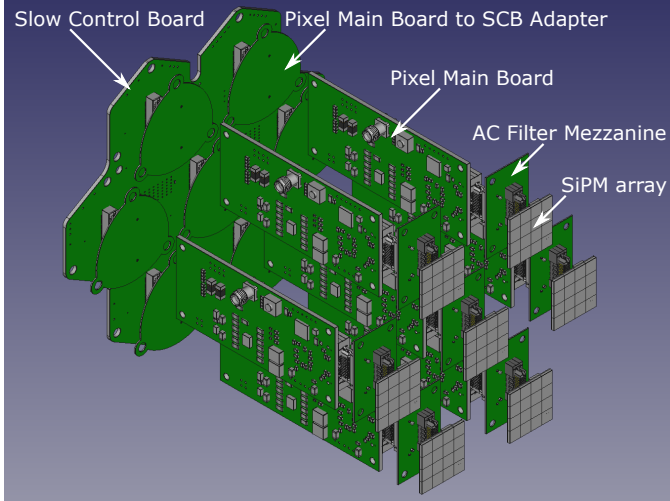
**Figure 2:** (a) SiPM array connection scheme. The signals from the inner twelve SiPMs are summed in the frontend electronics. One of the corner SiPM is used for calibration purposes and disconnected during normal operation. The three remaining corner SiPMs are not used. The dashed line shows the inner area of the Winston cone. (b) A picture of the FBK NUV HD3-4  $6\text{ mm} \times 6\text{ mm} \times 4 \times 4$  SiPM array used for the prototype.

MUSIC is developed at the Institute of Cosmos Sciences - University of Barcelona (ICCUB) [11]. The chip has been specifically developed to readout SiPMs providing 8 channels with an input bandwidth of 500 MHz. It provides two differential outputs each providing the sum signal of the eight input channels. The two outputs will be connected to the high gain and low gain lines respectively of the LST camera readout. The output signal can be shaped with an integrated tunable pole zero cancellation circuit, which we configured to yield the shortest signal full width at half maximum (FWHM). Indeed, unprocessed SiPM signals typically show a long tail due to the recharging of the triggered cells. With this configuration the peak of a one photoelectron signal corresponds to about 10 ADC channels.

Since the MUSIC ASIC has only eight channels, it is not possible to directly sum the signals of twelve SiPMs. We solved that problem by grouping the twelve SiPMs into pairs. The signals of one SiPM pair connect in parallel to one MUSIC channel thus using six of the eight MUSIC channels.

Figure 3 shows a rendering of the seven-pixel SiPM module. Starting from the left, we find the Slow Control Board (SCB), the only part of the LST electronic chain shown here. Seven SiPM Pixel main boards are connected to the SCB with the help of a printed circuit board (PCB) adapter. The pixel main board contains the SiPM bias regulator and the front-end electronics. A mezzanine can be added between the main board and the SiPM array to AC couple the SiPM signals into the front-end. In addition, each channel has a small board with a digital thermometer that is in thermal contact with the back of the SiPM array (here not shown).

A picture of the assembled seven-channel module is shown in Figure 4. In the next sections we discuss the design, test and measurements of a single module.



**Figure 3:** Rendering of the seven-channels module.

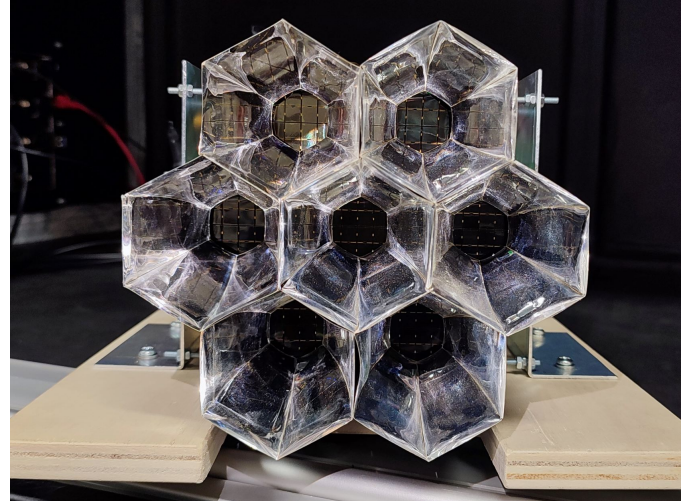
### 3. Pixel Design

#### 3.1. Block Diagram

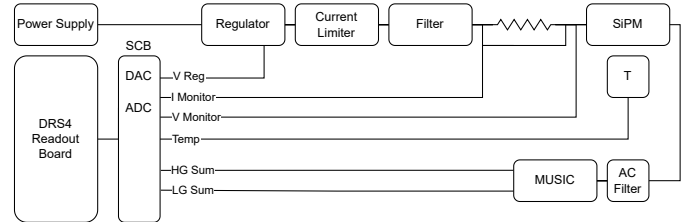
The block diagram of the SiPM Pixel is shown in Figure 5. As already mentioned, this prototype uses most of the LST readout chain, in particular the DRS4 readout board [8] and the SCB [12].

A *voltage regulator* (briefly described in section 3.2) is controlled by the 12 bit Digital to Analog Converter (DAC) located on the SCB. The regulator output connects a *current limiter* (section 3.3), limiting the current flowing into the SiPMs. Following the current limiter is a low-pass filter used to filter out noise on the SiPM bias line. Both the current and the bias voltage are monitored by the Analog to Digital Converter (ADC) on the SCB. The front-end electronics is based on the MUSIC ASIC [11], which deals with the summing and shaping of the signals. The regulator, the current limiter, the filter, the voltage and current monitors, and the MUSIC are located on the pixel main board. In order to be functional even in high background light level, it is possible to insert a high-pass filter in between the SiPMs and the MUSIC, which removes the DC baseline offset. This filter is located on a dedicated mezzanine, which is removable, to study the difference between DC and AC coupling in our application. The digital temperature sensor monitoring the SiPM temperature sends its data to the SCB and therefore to the control software. We use the temperature data to adjust the SiPM bias voltage thus correcting for the temperature dependent breakdown voltage of the SiPMs and guaranteeing stable sensor gain. In principle, this procedure corrects for any temperature increase, including SiPM power dissipation at high count rates.

The SiPMs draw more current than PMTs because they have a higher gain (typically  $\sim 10^6$  at operational overvoltage) and because they have a higher photon detection efficiency in the near infrared, where the night sky background is predominant. On this prototype, with these SiPMs biased at 6 V overvoltage, and without using a UV filter, we expect a current of about 1 mA during observations in dark condition. Since the SiPMs



**Figure 4:** Picture of the assembled seven-channel module; the mounted Winston cones are preliminary prototypes not used in the current telescope.



**Figure 5:** SiPM Pixel Block Diagram.

and front-end electronics require more power than the PMT modules, the SiPMs cannot be powered by the standard LST electronics. For our prototype we opted for a separate low noise bench power supply.

The DRS4 Readout Board connects to a backplane board, which communicates via Ethernet to a control computer running the LST data acquisition software. The acquired data are calibrated with the *cta-lstchain* pipeline, developed for the LST data analysis [13, 14].

In the next sections, we will describe the functionalities and the development of each block in our prototype SiPM pixel module.

#### 3.2. Voltage Regulator

The voltage regulator keeps the SiPM overvoltage constant over a large range of current flowing through the sensor. Its output voltage is set between 0 V and 48 V by the 12 bit DAC located on the SCB.

#### 3.3. Current Limiter

The current limiter is used to protect the MUSIC from a damaging high SiPM current. We demonstrated that a current of 3 mA divided over six MUSIC channels would not damage the device, and that it is preferable not to exceed this limit. The current limit can be relaxed in AC coupling mode, since it prevents the SiPM DC current from flowing into the MUSIC. Based on

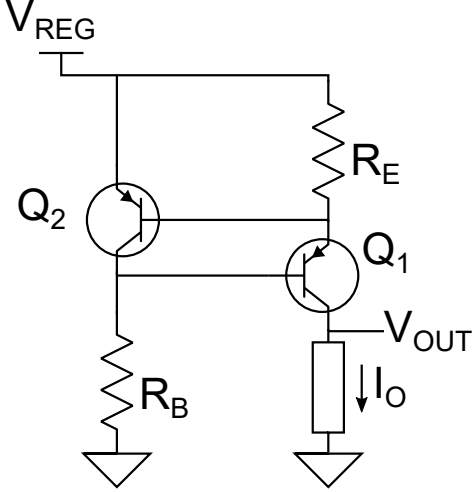


Figure 6: Current limiter.

Monte Carlo simulations, we expect a current of approximately 1 mA in dark conditions and up to 4 mA at the maximum NSB level at which the LST camera should operate. This factor already makes AC coupling a preferable option over DC coupling.

The easiest way to limit the current in a SiPM is to use a *protection resistor*. This approach is good for a low background detector. However, in applications where the background and thus the SiPM current fluctuates over wide margins like in IACTs, the same approach would result in large changes in bias voltage.

To limit the current we opted instead for a typical two-BJT current limiter circuit, shown in Figure 6. That solution results in a stable and basically current-independent bias voltage across the SiPMs.

When the current through  $R_E$  exceeds the design limit,  $Q_2$  turns on, thus turning off  $Q_1$ , therefore limiting the load current. If the two transistors have the same characteristics and are mounted nearby in the circuit, they will be at the same temperature, so we can say that they have the same current gain  $\beta$  and the same base to emitter voltage  $V_{BE}$ . The current is limited when both  $Q_1$  and  $Q_2$  are on; the output current  $I_O$ , which is the current flowing in the load  $R_L$ , is equal to:

$$I_O = \beta I_{B1} = \frac{\beta}{\beta + 1} (I_{B2} + I_{R_E}) = I_{MAX} \quad (1)$$

With simple calculations we obtain:

$$I_{MAX} = \frac{\beta}{(\beta + 1)\beta + 1} \left( \frac{V_{BE}}{R_E} (-\beta) + \frac{V_{REG} + 2V_{BE}}{R_B} \right) \quad (2)$$

In DC mode we want to limit the current to 3 mA, so we chose  $R_B = 10 \text{ k}\Omega$  and  $R_E = 220 \Omega$ . When operating in AC mode, the current limit can be higher (we choose 10 mA) and it is set adding a  $100 \Omega$  resistor in parallel to  $R_E$ .

For the actual circuit we had to choose a transistor with a collector-emitter breakdown voltage  $V_{(BR)CEO}$  above the SiPM bias voltage: we choose the *MMBT5401L*, whose minimum  $V_{(BR)CEO}$  is  $-150 \text{ V}$ .

### 3.4. Voltage and Current Monitors

The regulator output voltage and current are monitored by a voltage and a current monitor. The voltage monitor is an inverting amplifier, which, as sketched in Figure 5, reads the voltage directly from the SiPM cathode. The current monitor is a differential amplifier, which reads the voltage drop on a resistor placed between the Filter and the SiPM array. The amplifier outputs are connected to the ADC on the SCB.

### 3.5. MUSIC and Microcontroller

The MUSIC ASIC is controlled by an ATmega328P-AU microcontroller which runs the MUSIC MiniBoard firmware [15].

To ensure that the MUSIC bias is in its operational limits, two Low Dropout Regulators are mounted on the pixel main board, one for the 3.3 V line and one for the 5.0 V one.

### 3.6. High-Pass Filter

In high background applications, the signal lies over a quasi-DC noise. This DC baseline can be removed with a high-pass filter between the SiPM anode and the MUSIC input; we refer to this configuration as AC coupling. However, by adding this filter, we lose MUSIC's ability to individually adjust the SiPM bias voltages to compensate for the breakdown voltage difference in the array. The effect of the possible gain differences should be evaluated by further studies.

If  $R_F$  is the filter resistor, the voltage across the SiPM  $V_{SiPM}$  would be, considering only the DC component:

$$V_{SiPM} = V_{bias} - I_{SiPM} R_F \quad (3)$$

where  $V_{bias}$  is the SiPM bias voltage and  $I_{SiPM}$  is the current flowing into the sensor. Therefore, to keep constant the voltage across the SiPM, we need to actively adjust the bias voltage  $V_{bias}$  with a proper feedback loop, in order to compensate for the voltage drop on  $R_F$ . If the value of the  $R_F$  resistor is too high, a significant voltage drop will occur across it. Conversely, if the value of the resistor is too low, the signal loss would be too high because the resistor is in parallel with the MUSIC input. We chose  $R_F = 100 \Omega$ .

Regarding the filter capacitor  $C_F$ , we found in measurements that a  $1 \mu\text{F}$  AC coupling capacitor does not change the signal shape and, so we chose this value.

Each of the six channels contributing to the sum has the  $R-C$  filter previously described; the calibration pixel is instead directly connected, with no filter on its line.

### 3.7. Digital Thermometer

Since most of the SiPM characteristics depend on the over-voltage and due to the high temperature dependence of the breakdown voltage  $V_{bd}$ , it is necessary to precisely monitor the sensor temperature, in order to properly correct the bias voltage. For example, for the NUV HD3-4 we measured a  $dV_{bd}/dT$  equal to  $27 \text{ mV}/^\circ\text{C}$ . A  $1 \text{ cm} \times 1 \text{ cm}$  PCB with the digital thermometer is attached with thermal paste to the back of the SiPM array. The temperature values are sent to the LST electronics and from their recorded by the slow control software.

### 3.8. Design of the Pixel Main Board

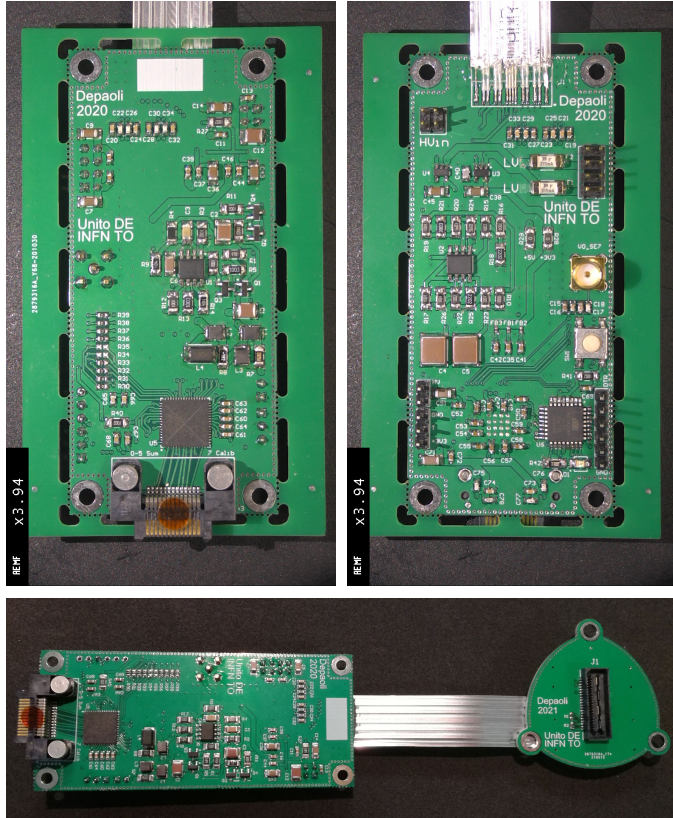
The design started with the MUSIC MiniBoard schematics and PCB layout, kindly made available by the MUSIC developers which made it possible to use the MiniBoard firmware in our design. The main board is a six-layer PCB, with the two inner layers dedicated only to the high-speed sum signals (one for the High Gain, the other one for the Low Gain). Most of the components were assembled at Asseltech Srl, Chivasso, Piedmont, Italy, and the remaining part at the Electronics Laboratory of the National Institute for Nuclear Physics (INFN), Turin section. Pictures of the assembled board are shown in Figure 7.

### 3.9. Camera Control Software

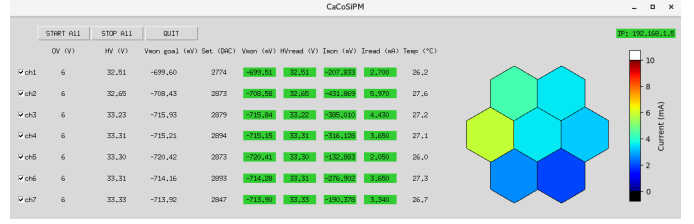
The Camera Control Software for the SiPM Pixel (*CaCoSiPM*) is the software that takes care of the module slow control. It runs on a computer connected via Ethernet to the DRS4 readout board and is based on the software developed by the LST collaboration to control the LST modules. The python-based GUI designed to control the system is shown in Figure 8.

#### 3.9.1. Normal Operation

At start-up, the SiPM voltage is set above zero, but below the breakdown to keep it from producing signals but still have it reverse biased. This is done to avoid damaging the MUSIC with the high current that could flow through the SiPM junction if forward biased.



**Figure 7:** Top: pictures of the pixel main board. Bottom: pixel main board connected to the Slow Control Board Adapter PCB.



**Figure 8:** CaCoSiPM user interface.

The software will activate the channel, adjust the SiPM bias voltage to the desired level, and maintain it through a feedback loop operating at 1 Hz. The feedback works in the following way: the software evaluates the required  $V_{HV}$  corresponding to the desired overvoltage, then calculates the corresponding voltage monitor  $V_{mon}$  value and changes the DAC value to reach the desired  $V_{mon}$  (one DAC bit corresponds to about 8 mV SiPM bias voltage). The regulator output voltage  $V_{HV}$  is calculated as follows:

$$V_{HV} = V_{OV} + V_{bd}(T_0) + \frac{dV_{bd}}{dT}(T - T_0) + V_{anode} \quad (4)$$

Where  $V_{anode} = V_{MUSICIN}$  in the DC and  $V_{anode} = R_{AC} * I_{mon}$  in the AC coupling.  $V_{OV}$  is the desired SiPM overvoltage at which we want to operate the pixel, 6 V in our prototype.  $V_{bd}(T_0)$  is the average breakdown voltage of the twelve SiPMs in the array forming the camera pixel at the reference temperature  $T_0$ . The third term is the temperature compensation of the SiPM breakdown voltage, which increases by  $dV_{bd}/dT$ . The last correction  $V_{anode}$  depends on the SiPM coupling (AC or DC) and is introduced since in both cases the SiPM anode voltage is higher than zero. In the DC case, we have seen that the SiPM anode voltage is equal to the MUSIC input pin voltage  $V_{MUSICIN}$ . In the AC case the situation is instead a bit more complicated. The DC current flowing through the SiPM causes a voltage drop across the filter resistor:

$$V_{RF} = I_{SiPM} R_F \quad (5)$$

where  $I_{SiPM}$  is the current flowing through the SiPM. By design, the current read by the Current Monitor  $I_{mon}$  is the sum of the currents flowing into the twelve SiPMs; since every two SiPMs are connected in parallel, we have six channels in total. With good approximation, the current splits equally between the six channels, and thus  $V_{anode}$  will be:

$$V_{anode} = \frac{I_{mon}}{6} R_F \quad (6)$$

The software evaluates therefore the value of  $V_{HV}$  and, through a feedback loop, adjusts the DAC value accordingly to achieve the desired bias voltage for the SiPM.

## 4. SiPM Pixel Measurements

The seven-pixel module is kept in a light-tight dark box. The CaCoSiPM software controls and monitors the SiPM bias voltage; all the measured values are saved in a log file. A DC LED controlled with a power supply simulates the night sky

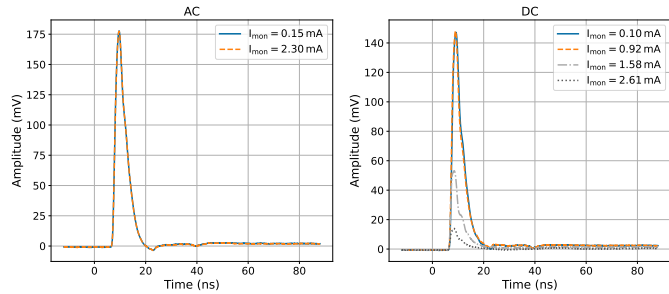
background; by increasing the LED bias it is possible to scan different NSB-equivalent intensities, corresponding to different currents flowing into the SiPM array. The sensor is also illuminated with a pulsed sub-nanosecond LED (Picoquant PDL 800-D with PLS 400 LED head), simulating a fast Cherenkov flash.

#### 4.1. Comparison Between AC and DC Coupling

From laboratory measurements, shown in Figure 9, where on the left we have the High Gain signals acquired in AC coupling, and on the right in DC coupling, we observed that in the DC approach the MUSIC output drops as soon as the combined current of all SiPMs connected to the same MUSIC exceeds 1 mA. Signals are acquired, thanks to a custom adapter, by means of a Tektronix MSO6 Oscilloscope with 1 GHz bandwidth and with a 100 ns acquisition window. Each waveform is the average of 1000 acquisitions, and is labelled with the corresponding current flowing in the channel, measured with the Current Monitor. Thus, the AC coupling is the best solution for our application, where working in a high-brightness background environment is a critical aspect.

In addition, the current limiter hardware settings can be relaxed if we work in AC mode, since the DC current due to the night sky background would flow to the ground through the filter resistor.

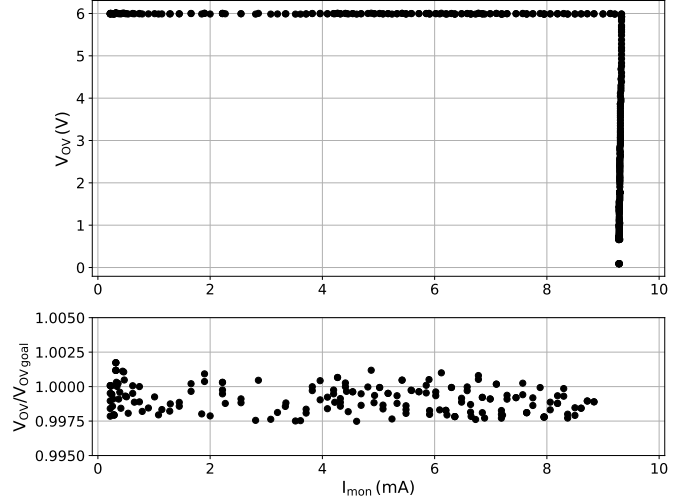
From now on all the reported measurements are done in AC coupling mode and with the current limiter set to 10 mA.



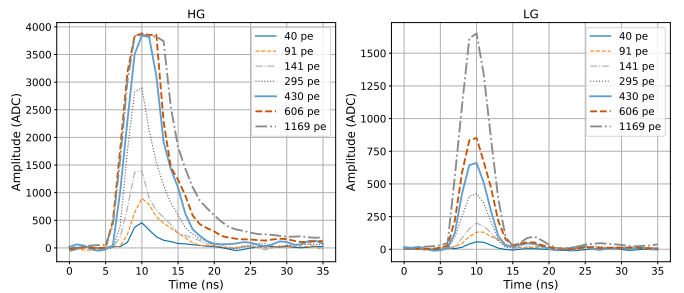
**Figure 9:** Signals acquired in AC (left) and DC (right) coupling at different light backgrounds; in both plots the solid and dashed waveforms are overlapping.

#### 4.2. Current Limiter Test

The results of the current limiter tests are shown in Figure 10 for a single channel, where  $I_{mon}$  is the current flowing in the SiPMs and  $V_{OV}$  is the SiPM overvoltage. The datapoints at the lowest value for  $I_{mon}$  are obtained by switching the LED off. The non-zero  $I_{mon}$  current is due to SiPM intrinsic dark counts. Turning on the LED and gradually increasing its brightness, we can see how the SiPM overvoltage remains constant within about 0.25 %, until it drops sharply when the hardware-set limit is reached. The last points, the ones at the lowest  $V_{OV}$  values, are taken with the dark box open, exposing the switched-on detector to daylight proving its robustness.



**Figure 10:** Top: SiPM overvoltage dependence on current. Bottom: ratio of measured overvoltage and desired one before current limit intervention.



**Figure 11:** Examples of High Gain (left) and Low Gain (right) SiPM Pixel calibrated waveforms acquired with the LST electronics; waveforms of the same line style refer to the acquisition of the same light pulse intensity.

### 4.3. Waveform acquisition

The SiPM Pixel signal acquisition is based on the LST readout electronics. The High Gain and Low Gain signals are acquired with the control computer connected via Ethernet to the DRS4 readout board. The signal processing relies on the *ctastchain* software package. Examples of High Gain (a) and Low Gain (b) waveforms at different pulsed LED intensities are shown in Figure 11. Waveforms of the same line style refer to the acquisition of the same photon pulse intensity; the number of photoelectrons reported in the labels is obtained from the calibration factors that will be discussed later.

Although the High Gain signal has a longer tail than the Low Gain one due to the different amplifiers in the two channels on the LST electronics board, this has no effect on the measured FWHMs:

$$FWHM_{HG} \approx FWHM_{LG} \approx 4.3 \text{ ns} \quad (7)$$

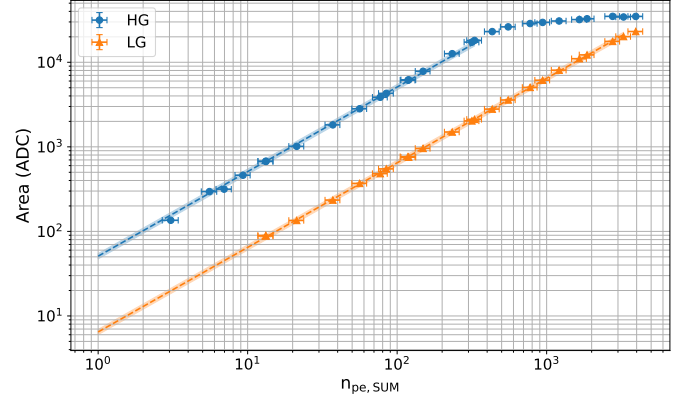
This result is similar to what we measured for the PMT ( $\sim 3$  ns for the High Gain and  $\sim 3.5$  ns for the Low Gain), so we expect similar performance, which should be confirmed by a detailed Monte Carlo simulation.

### 4.4. Pixel Calibration

The twelve SiPMs constituting one camera pixel are connected two by two in parallel to six MUSIC ASIC channels, which sums and shapes their signals. It is impossible to distinguish single photoelectron signals in the sum signal, neither in High Gain nor Low Gain. It is therefore necessary to find another solution to calibrate the pixel. We connected one of the unused MUSIC channels to one of the four SiPMs at the corners of the array, normally disconnected, since we are able to see single photoelectron signals on this channel and we can use it to cross-calibrate the whole pixel. The signal from this SiPM can be accessed from the corresponding single-ended (SE) MUSIC output and it is not shaped by the pole-zero cancellation circuit, thus preserving its amplitude. The Differential Leading Edge Discriminator (DLED) technique [16] is used to deal with the long SiPM tails in the recorded signal traces.

The SE signal from the reference pixel is digitized with an oscilloscope.

During the calibration procedure, the SiPM pixel (without the Winston cones) is illuminated with a uniform pulsed light. The light uniformity is obtained by means of an optical diffuser, and has been evaluated to be on the order of 7%. The pulsed LED triggers the acquisition of the SE reference signal and the High Gain and Low Gain sum signals. In this configuration, if the reference pixel is hit by an average number of photons  $n_{ph,SE}$ , the twelve-SiPM pixel, simply by scaling to the different effective sensor areas, will detect on average twelve times more photons. With good approximation, all SiPMs in the  $4 \times 4$  array have the same photon detection efficiency. Therefore, the number of detected photons, i.e. photoelectrons, is also twelve times higher in the combined twelve-SiPMs as compared to the SE SiPM. Therefore, by knowing the average number of photoelectrons generated in the reference SiPM, we can calibrate the whole pixel. If  $A_{HG}$  and  $A_{LG}$  are the average area of the High



**Figure 12:** SiPM pixel calibration and linearity results. The error bands take into account the statistical error and the light non-uniformity.

Gain and Low Gain sum signals respectively, the HG and LG gains are defined as:

$$\begin{aligned} g_{HG} &= \frac{A_{HG}}{n_{pe,SUM}} \approx \frac{A_{HG}}{12 n_{pe,SE}} \\ g_{LG} &= \frac{A_{LG}}{n_{pe,SUM}} \approx \frac{A_{LG}}{12 n_{pe,SE}} \end{aligned} \quad (8)$$

As a first step, we checked that the amplitudes of the sum signals do not change when going from operation to calibration mode. Then the reference pixel is calibrated and its linearity is studied.

The calibration procedure is repeated for different pulsed LED light intensity, leading to the results shown in Figure 12. The High Gain channel saturates at about 300 photoelectrons, while the Low Gain is linear up to 2000 photoelectrons. Data in the linear region are fitted with:

$$\begin{aligned} A_{HG} &= g_{HG} n_{pe,SUM} \\ A_{LG} &= g_{LG} n_{pe,SUM} \end{aligned} \quad (9)$$

where  $A_{HG}$  and  $A_{LG}$  are the integrated High Gain and Low Gain SUM signals and  $n_{pe,SUM}$  the number of photoelectrons. Once  $g_{HG}$  and  $g_{LG}$  are found, it is possible to convert every signal into number of photoelectrons.

Further studies would be needed to estimate all systematics effects and the time evolution of the calibration factors in case the module is mounted on the telescope.

## 5. Conclusions

In this work we developed and built an SiPM-based prototype module for the Large-Sized Telescopes of the Cherenkov Telescope Array. In our development we focused on demonstrating the advantages of SiPMs over PMTs in our application, i.e. their robustness and tolerance to high illumination levels. In principle, with these sensors it would be possible to observe under illumination conditions that are too bright for traditional photomultipliers, thus leading to an increase in the duty cycle



of the experiment. In addition, their robustness would improve the reliability of the telescope: indeed, accidentally exposing the PMTs to daylight, even with their high voltage off, can seriously damage them. In particular, using an active current limiter instead of a protection resistor proved to be essential to maintain the same overvoltage at different night sky background levels, and the AC coupling approach, reducing the current flowing through the ASIC, allows us to see signals even in high background conditions. Using the MUSIC's shaping circuit, we obtained a fast signal for the SiPM module, similar to the PMT one. The solutions described in this work can be used in a future LST SiPM-based camera, or more in general in any other SiPM applications characterized by a high background noise.

## Acknowledgments

We are grateful for financial support from organizations and agencies listed in [https://www.cta-observatory.org/consortium\\_acknowledgments/](https://www.cta-observatory.org/consortium_acknowledgments/) and from "Dipartimento di Eccellenza, Università degli Studi di Torino".

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